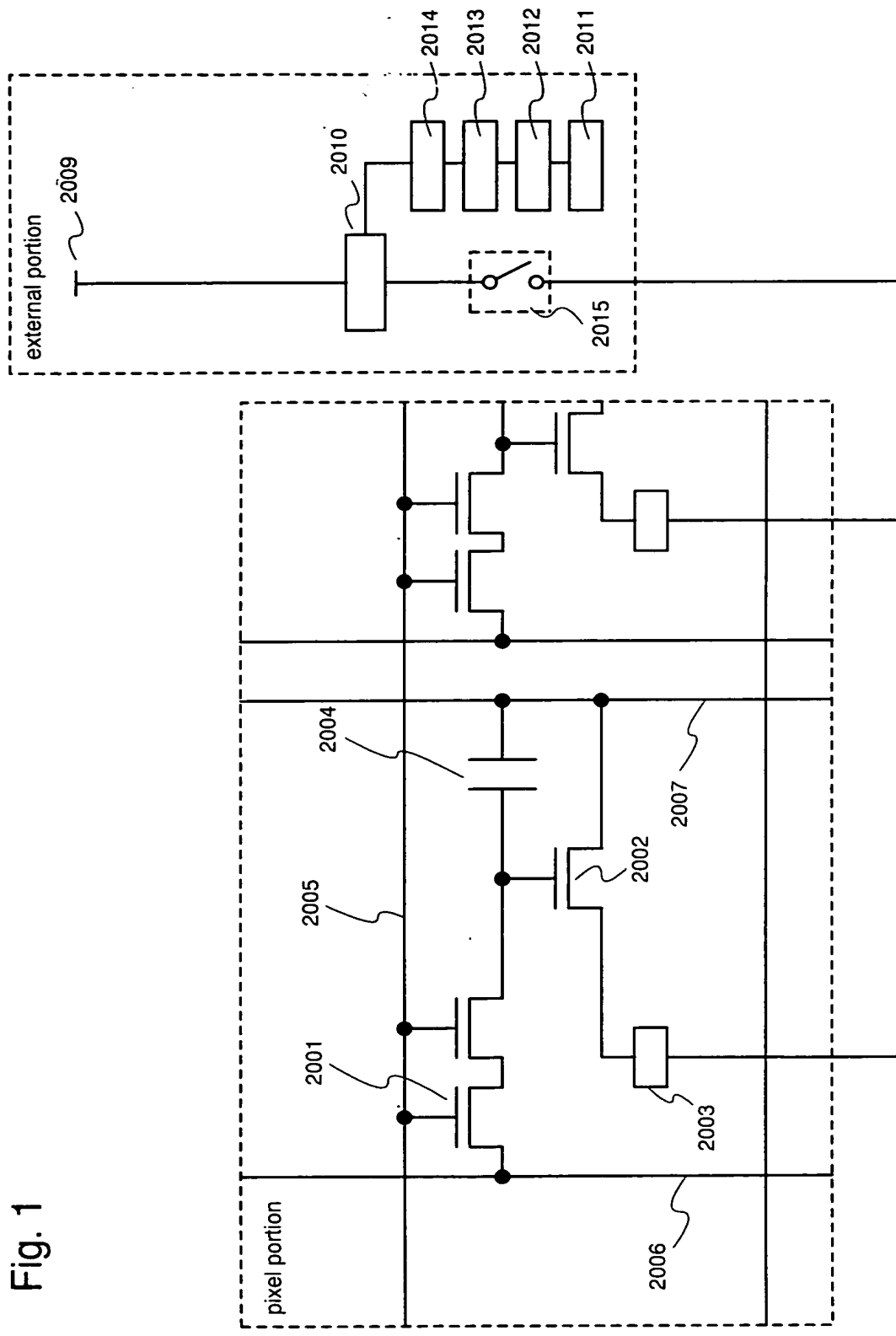


Fig. 1



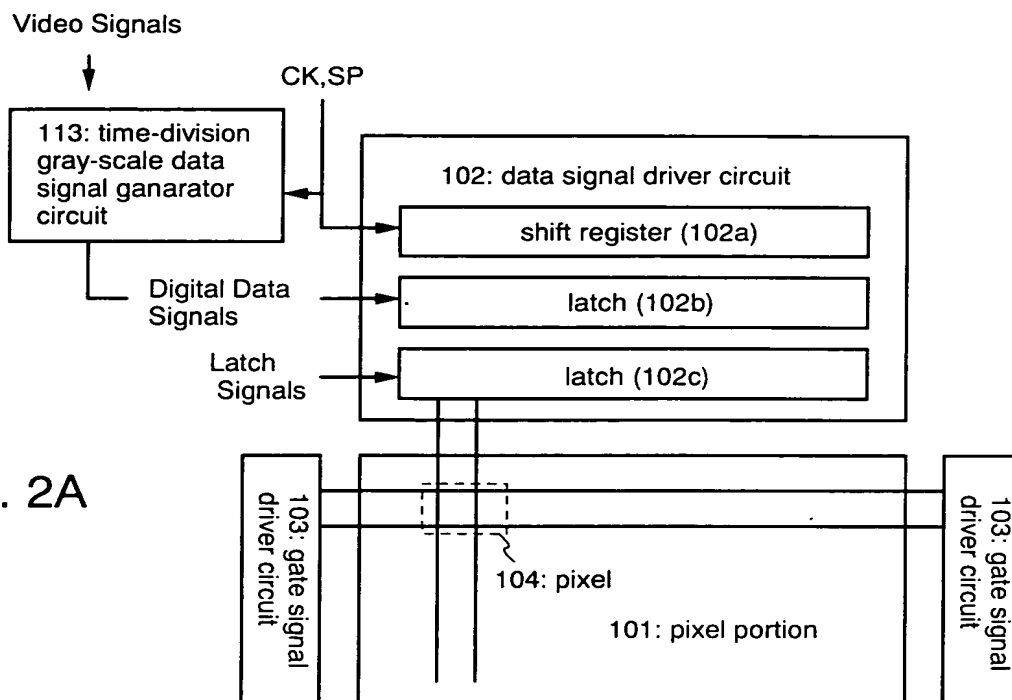


Fig. 2A

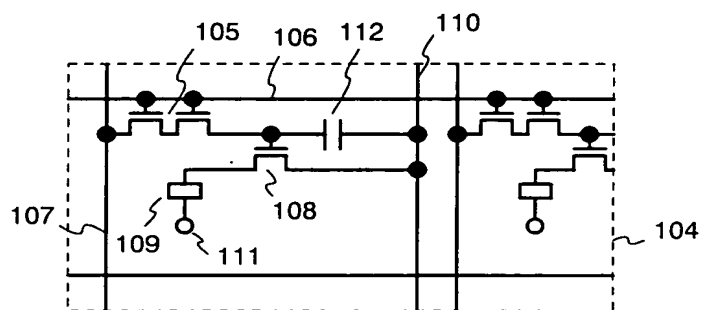


Fig. 2B

Fig. 3

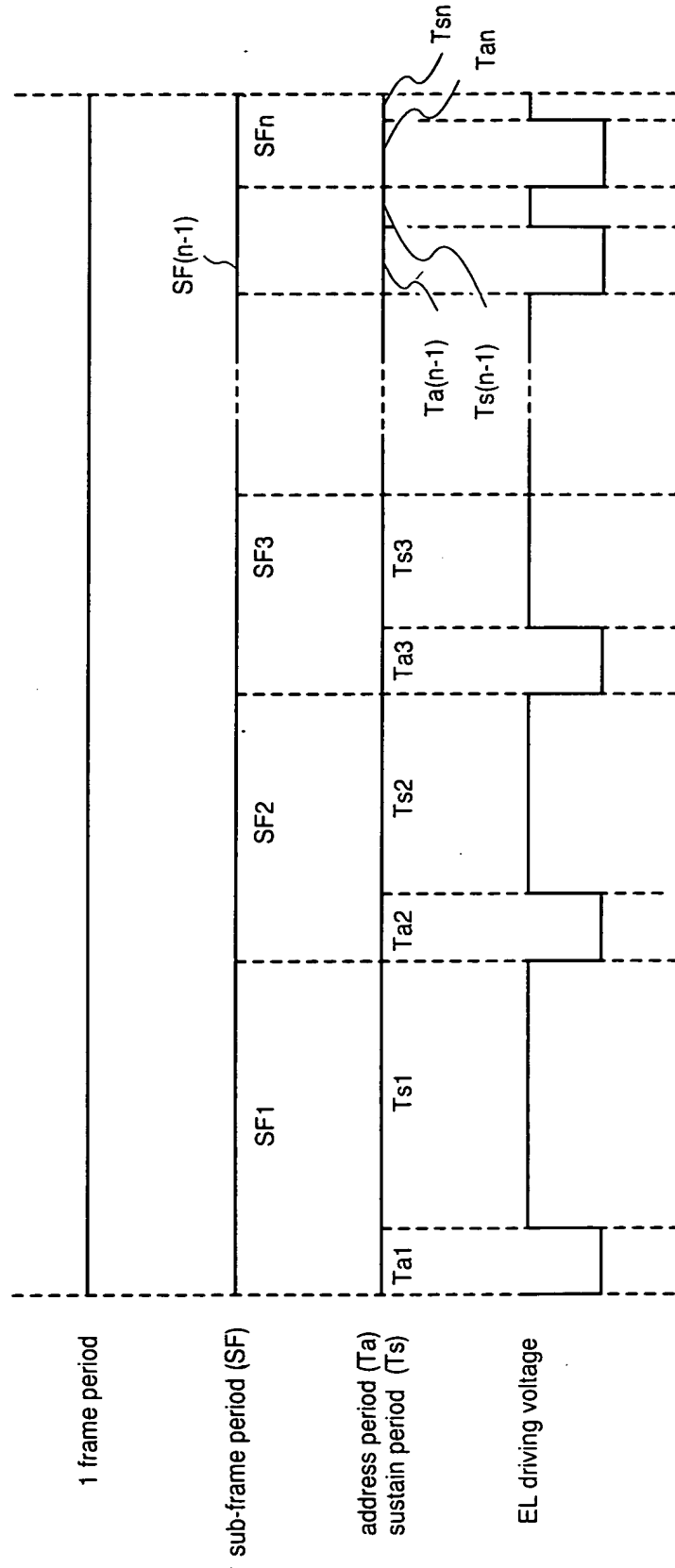


Fig. 4

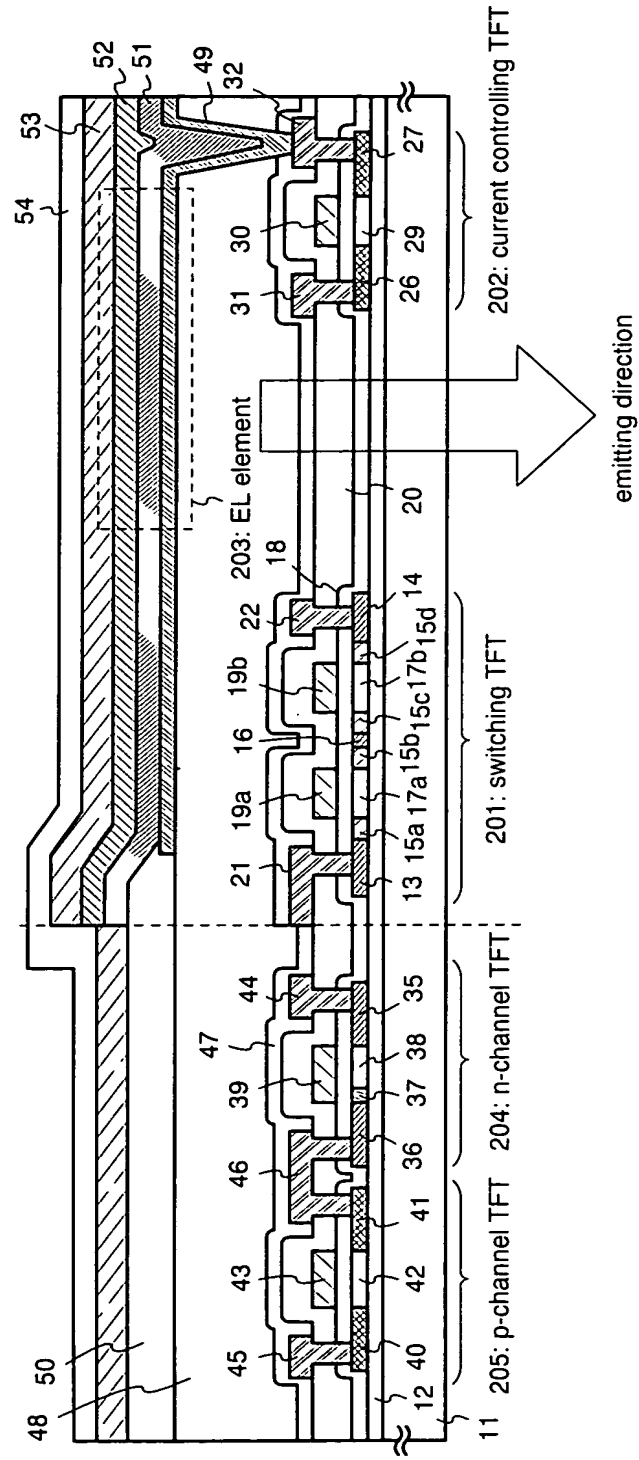


Fig. 5

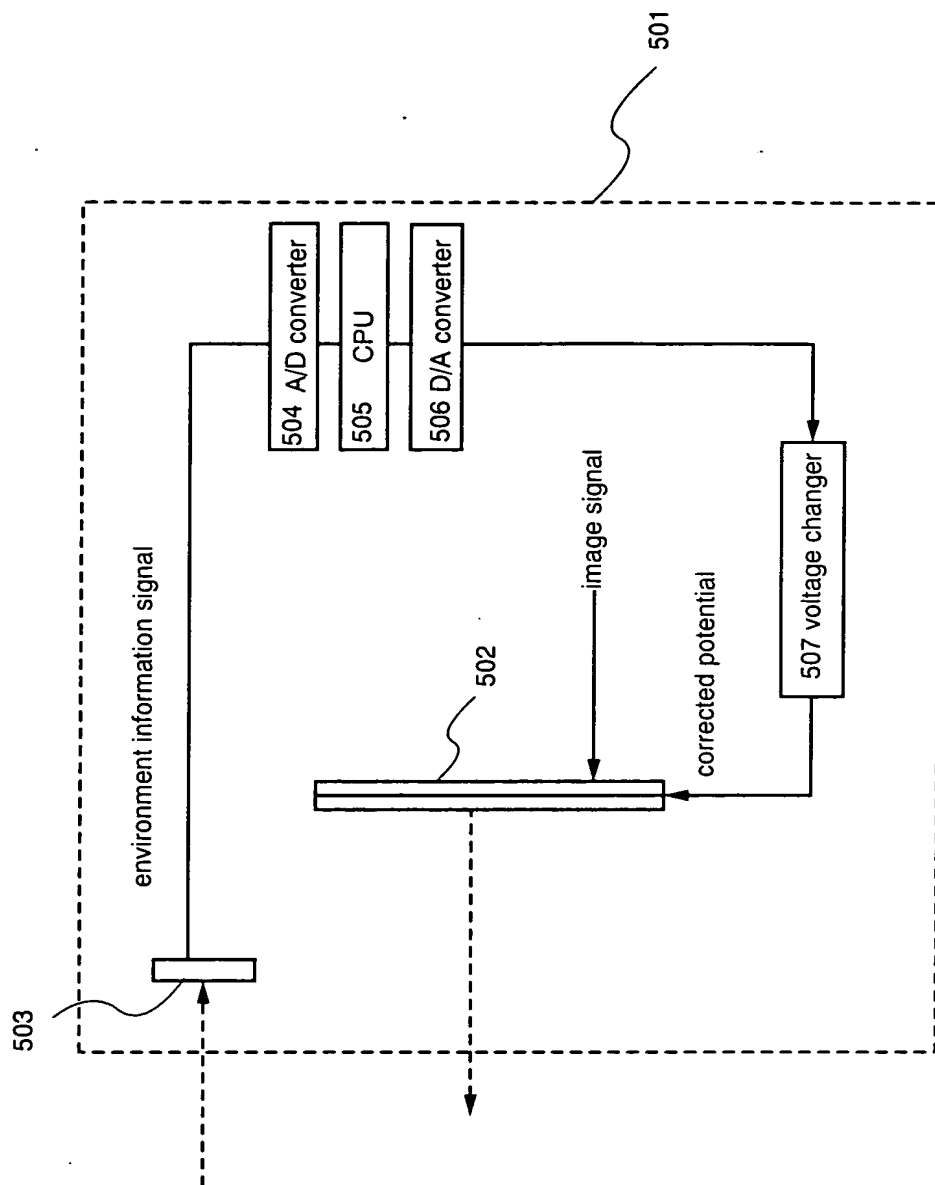


Fig. 6

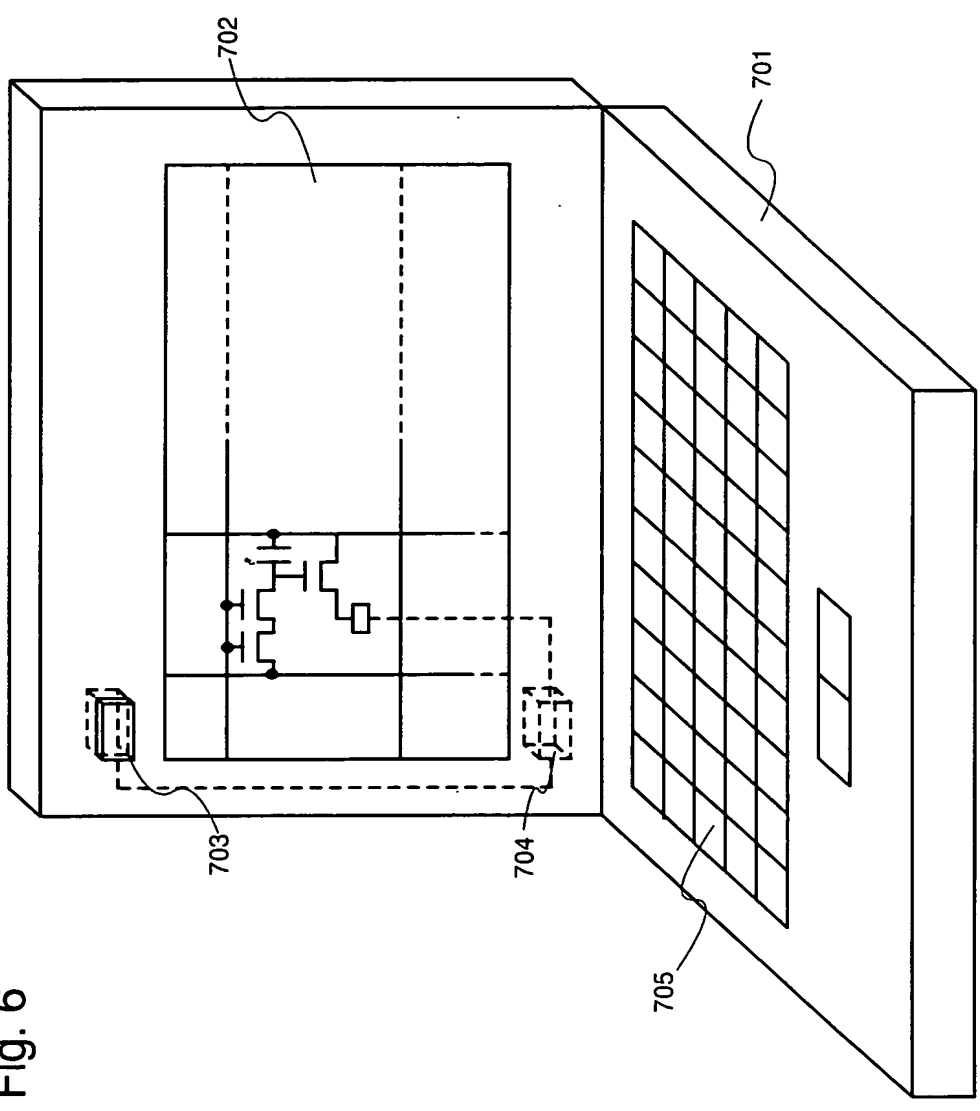


Fig. 7

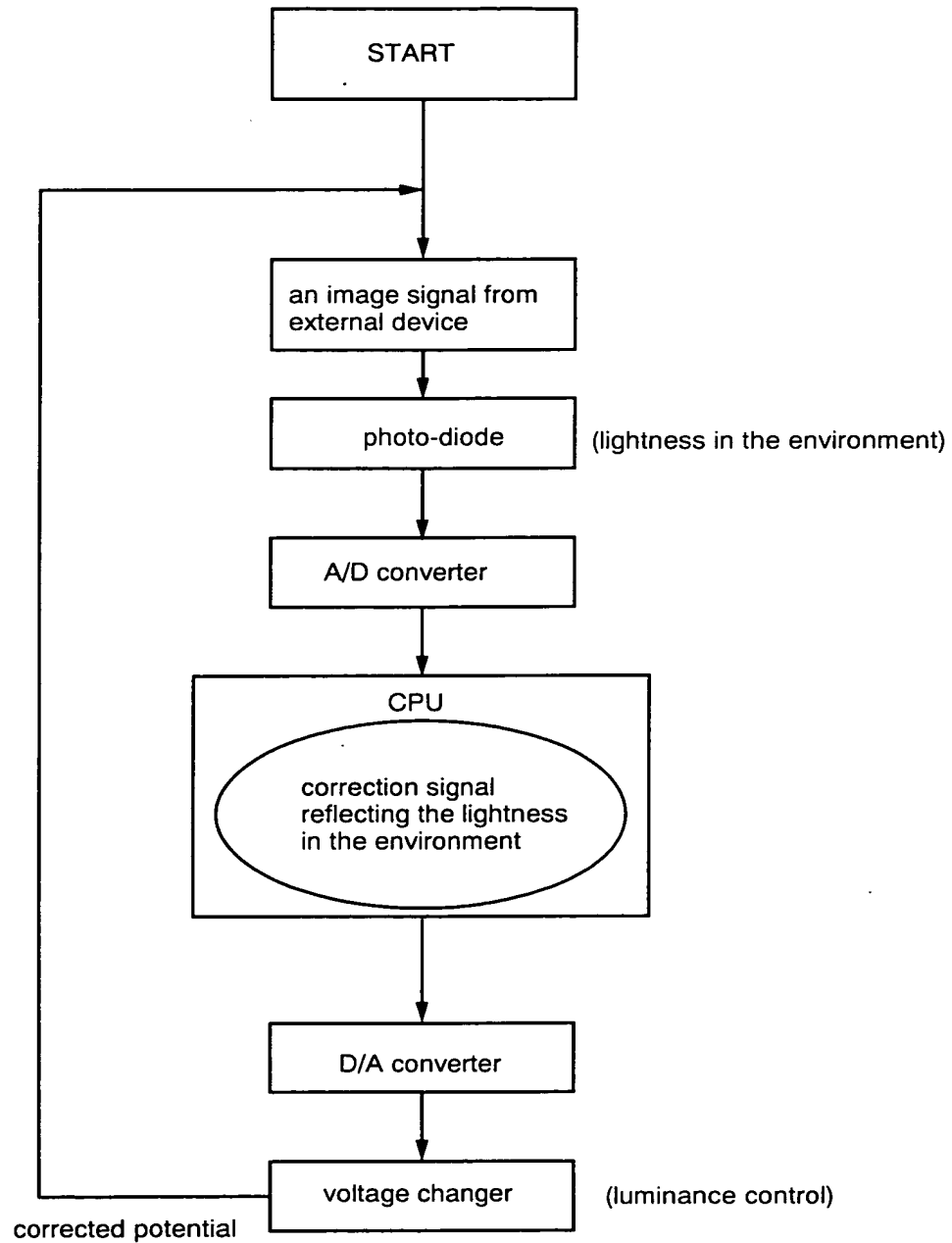
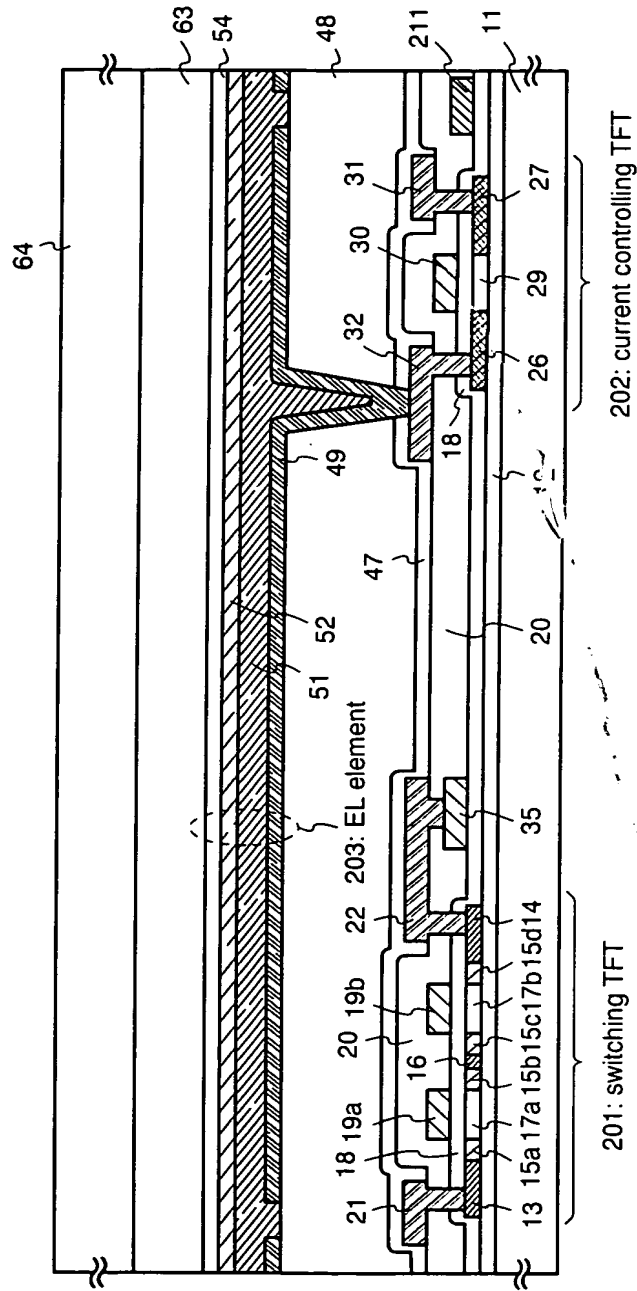


Fig. 8



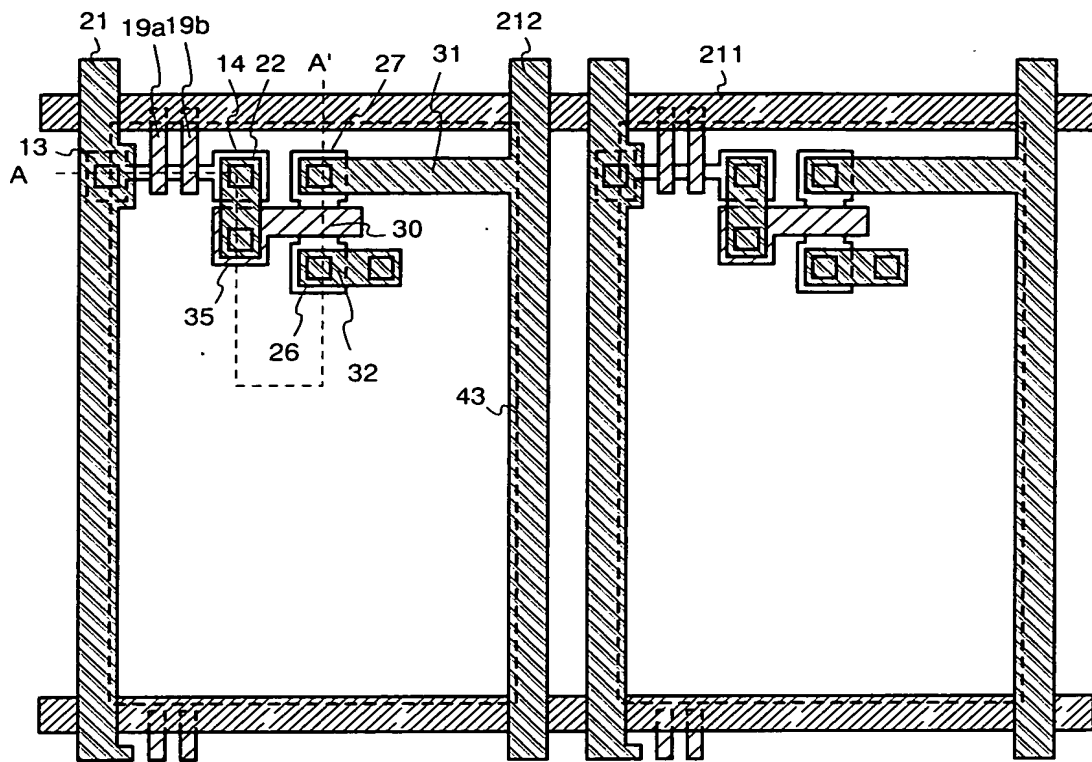


Fig. 9A

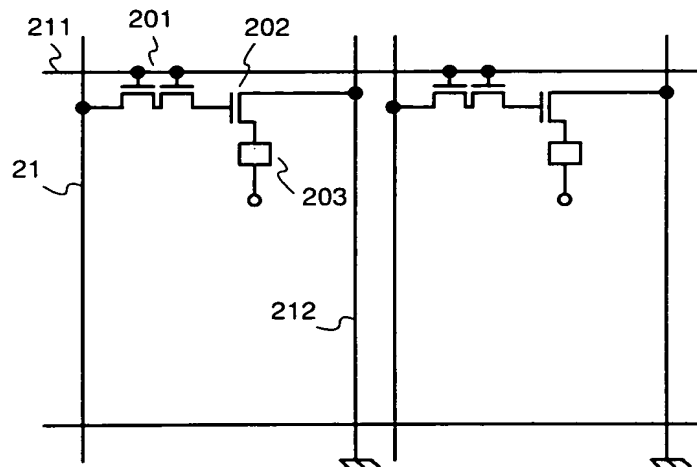


Fig. 9B

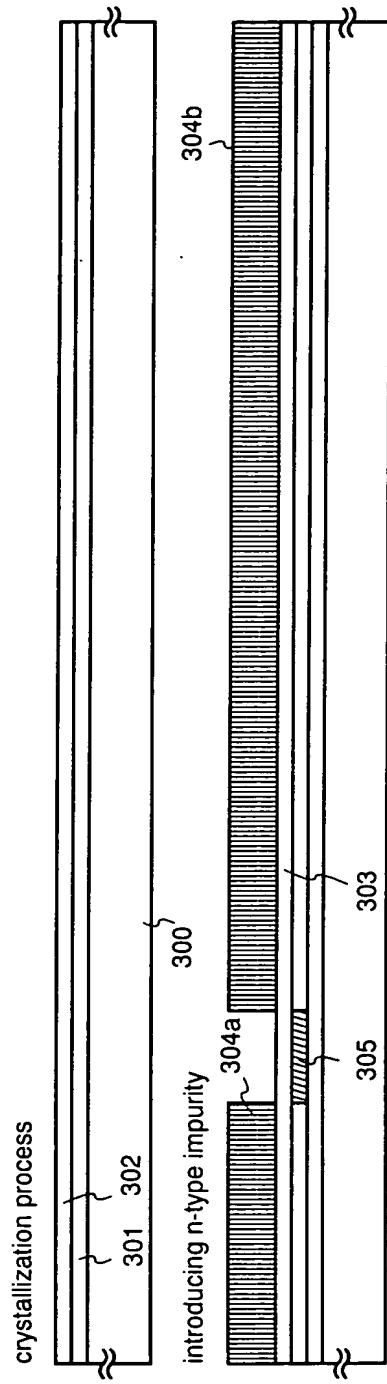


Fig. 10A

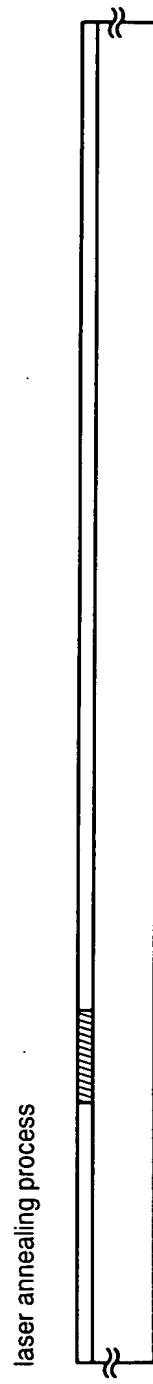


Fig. 10C

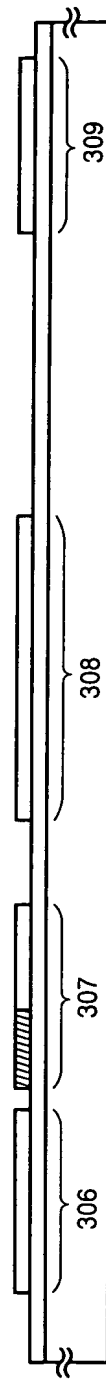


Fig. 10D

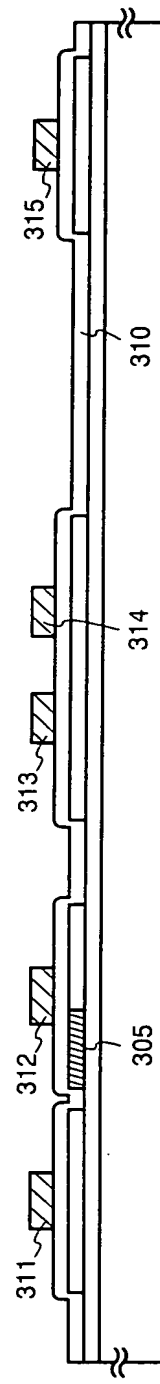


Fig. 10E

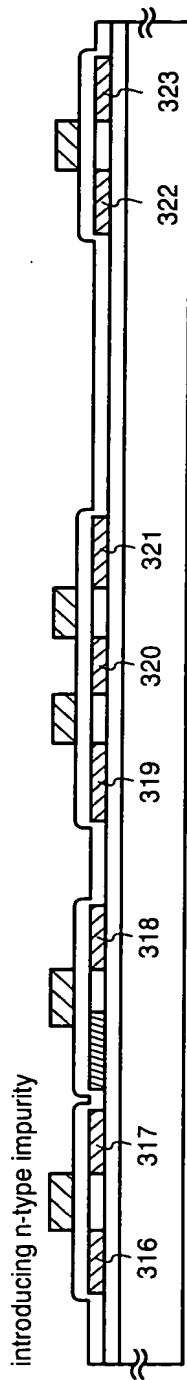


Fig. 11A

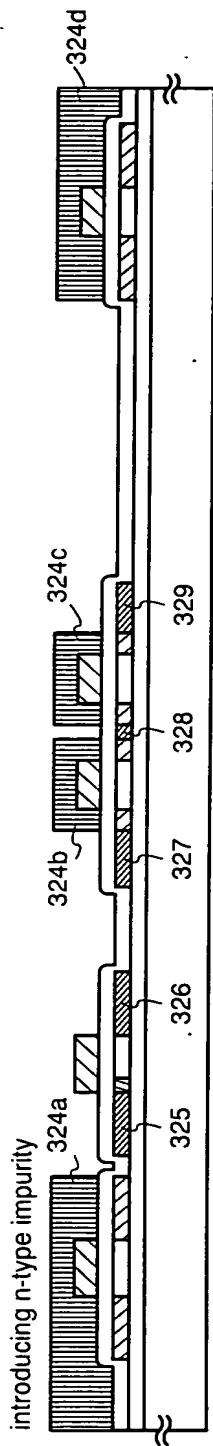


Fig. 11B

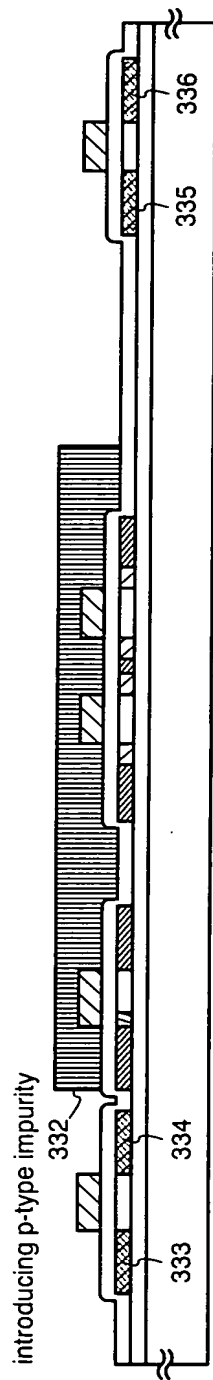


Fig. 11C

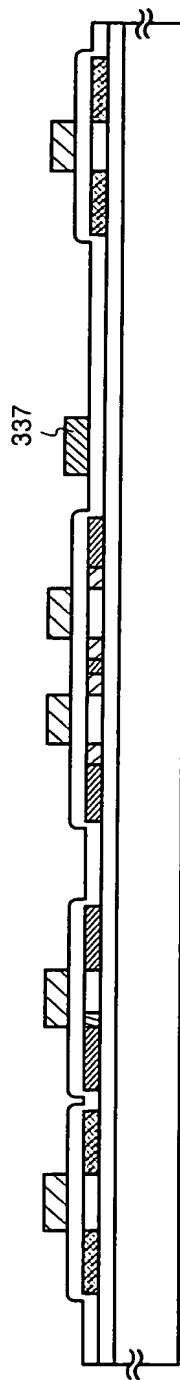


Fig. 11D

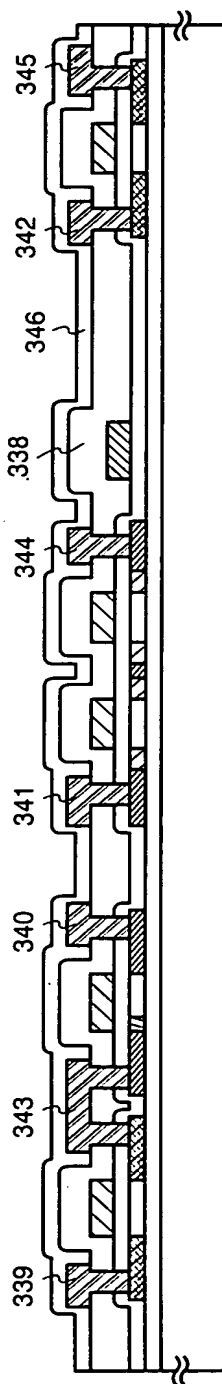


Fig. 12A

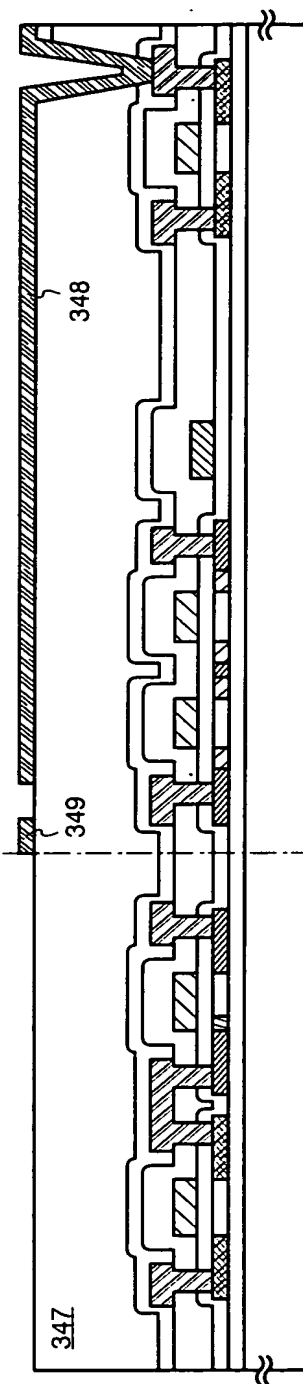


Fig. 12B

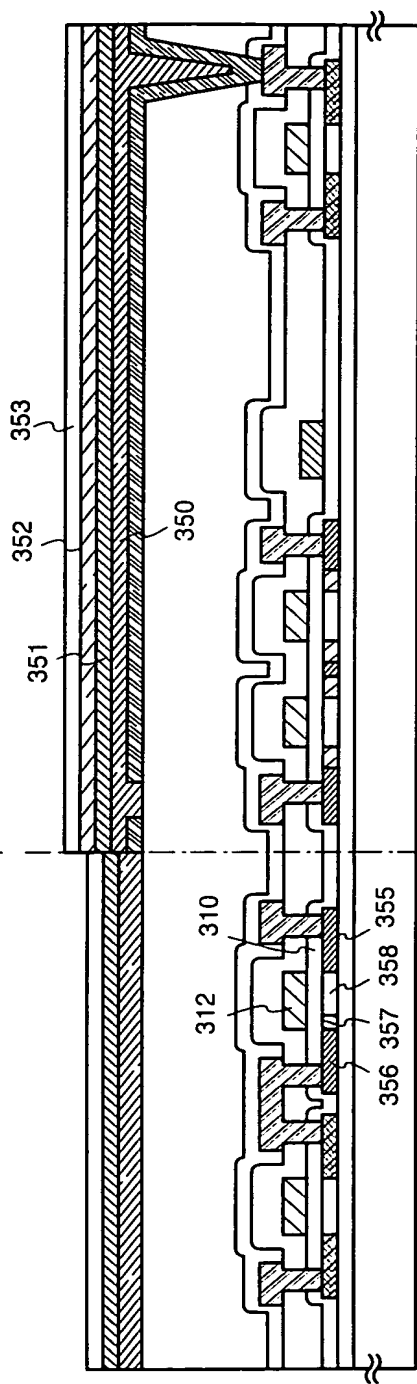


Fig. 12C

206: p-channel TFT 205: n-channel TFT

Fig. 13

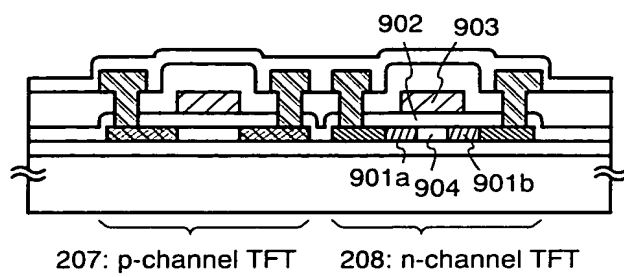
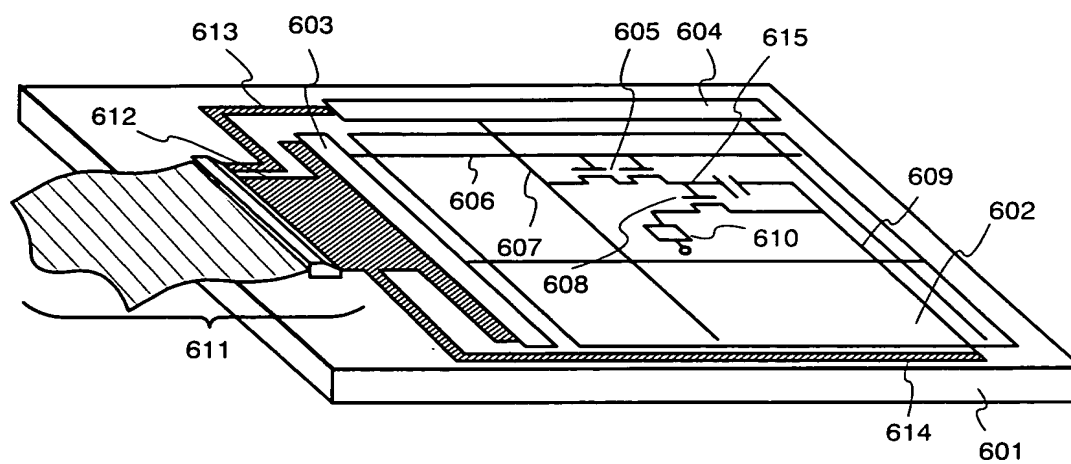


Fig. 14



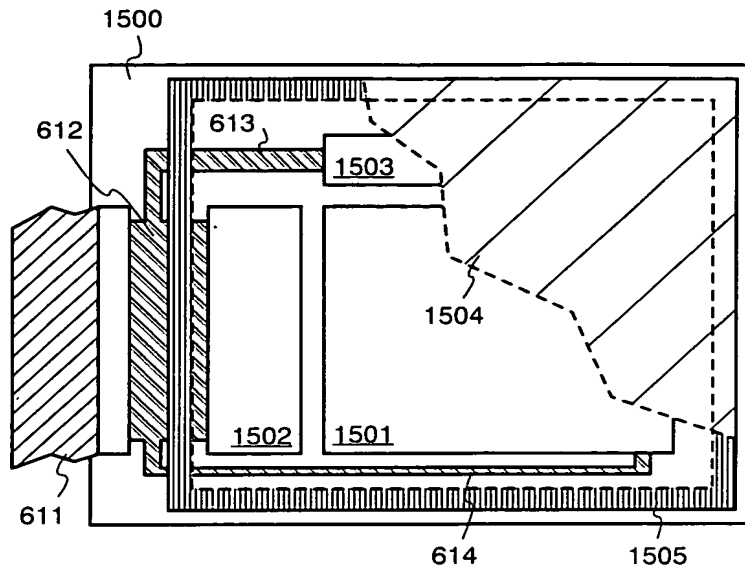


Fig. 15A

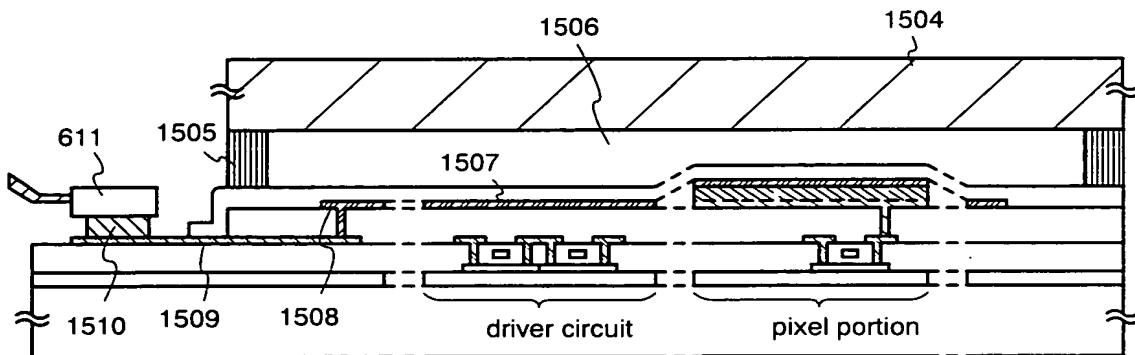


Fig. 15B

Fig. 16

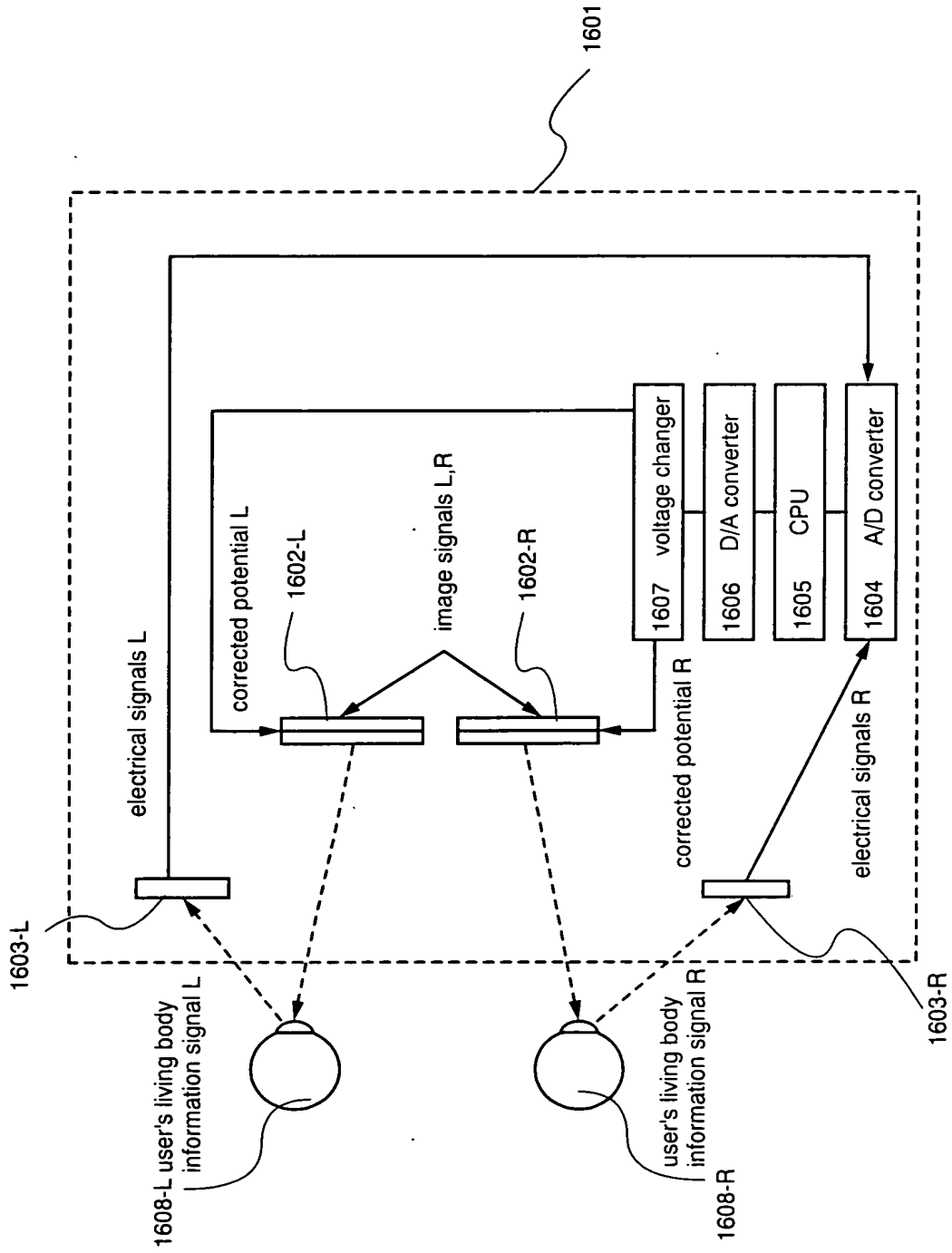


Fig. 17

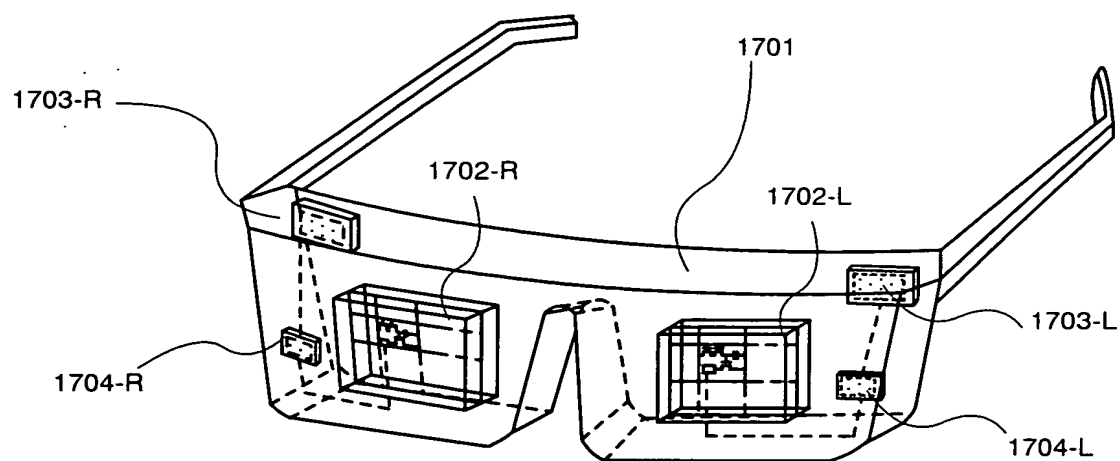
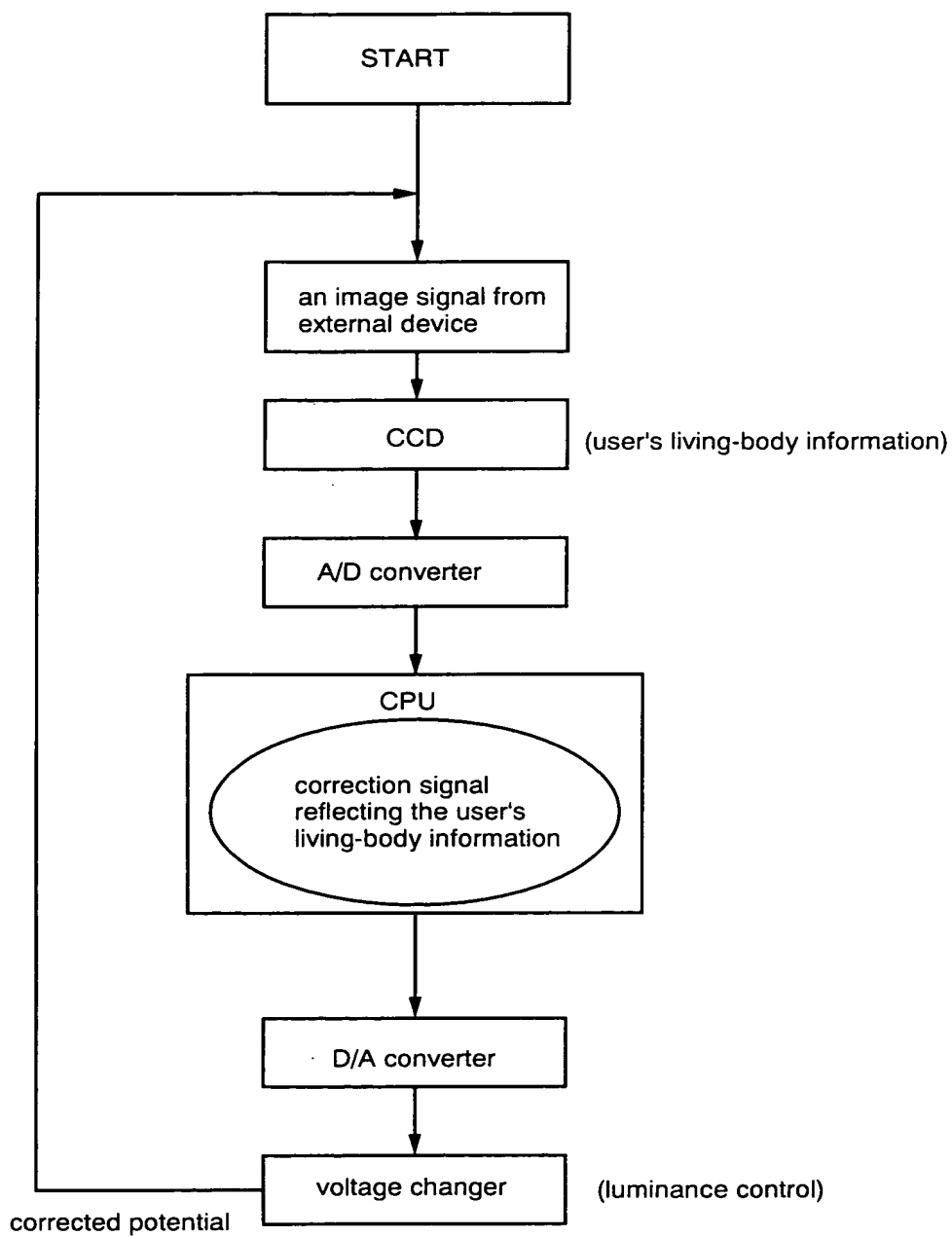
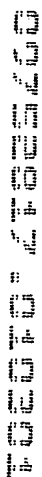
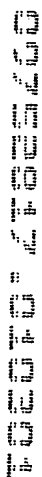
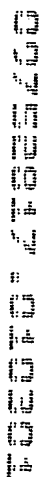


Fig. 18



[illegible][illegible][illegible]

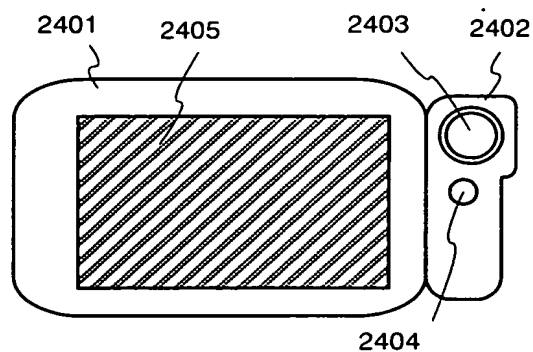
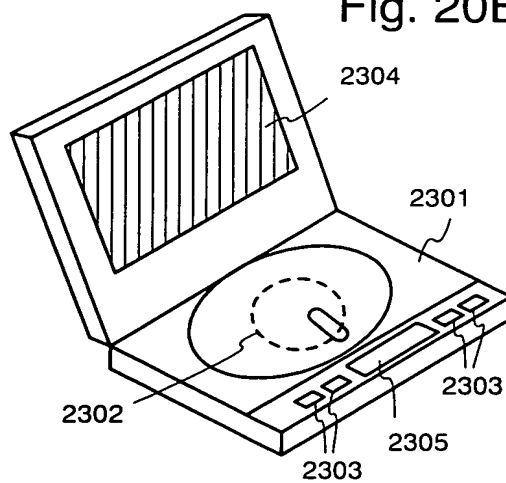
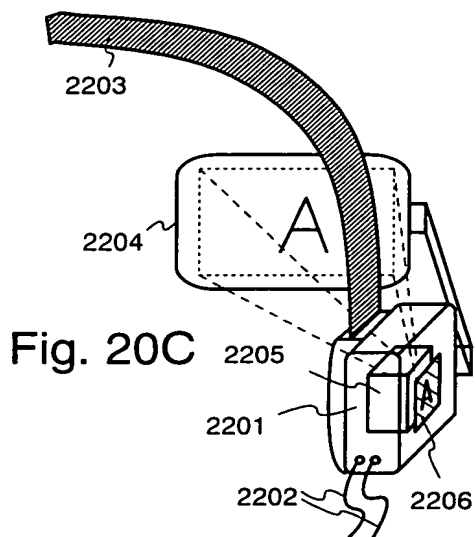
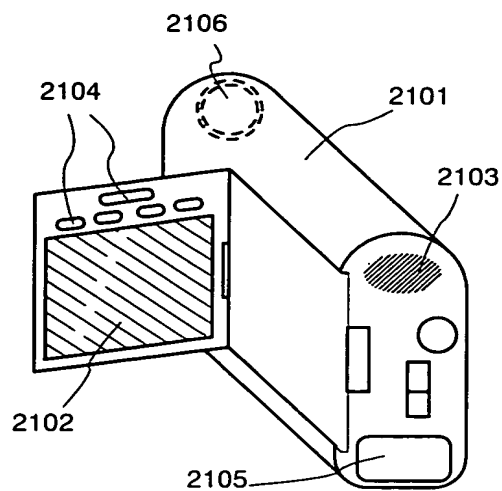
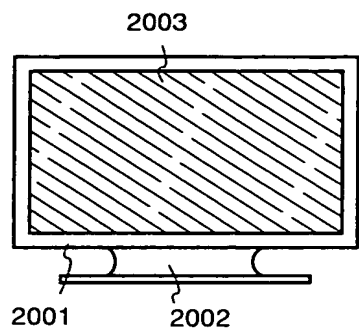


Fig. 21A

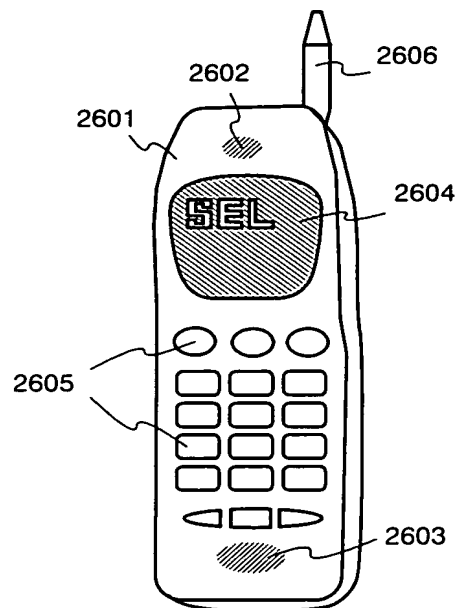


Fig. 21B

